	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
1	BRS	L1	25023	soi silicon-on-insulator silicon adj insulator thin adj film adj transistor$2 tft	USPAT; US-PGP UB	2002/11/28 12:41		
2	BRS	L2	35266	soi silicon-on-insulator silicon adj insulator thin adj film adj transistor$2 tft	EPO; JPO; DERWEN T	2002/11/28 12:42		
3	BRS	L3	1040	1 and (1dd low$2 adj impurity mdd)and spacer$2	USPAT; US-PGP UB	2002/11/28 12:58		
4	BRS	L4	0	((silicide) near31 (source and drain)adj (electrod$2 wir$4))and 3	USPAT; US-PGP UB	2002/11/28 12:52		
5	BRS	L5	3	((silicide) same (source and drain)adj (electrod$2 wir$4))and 3	USPAT; US-PGP UB	2002/11/28 12:45		
6	BRS	L6	0	((silicide) same(source and drain)adj (electrod$2 wir$4))and 3	USPAT; US-PGP UB	2002/11/28 12:47		
7	BRS	L7	17	((silicide) near31 (source and drain)and((source and drain)adj (electrod$2 wir$4))and 3	USPAT; US-PGP UB	2002/11/28 12:47		
8	BRS	L8	20	2 and (1dd low$2 adj impurity mdd)and spacer$2	EPO; JPO; DERWEN T	2002/11/28 12:48		
9	BRS	L9	0	((silicide) and ((source and drain)adj (electrod$2 wir$4))and 8	EPO; JPO; DERWEN T	2002/11/28 12:52		